Sh et 1 of 1 RM PTO-1449 (SUBSTITUTE) Attorney Docket No.: Applic. No. MUH-12823 10/685,064 S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Applicant Peter Weitz INFORMATION DISCLOSURE STATEMENT BY APPLICANT Filing Date **Group Art Unit** (37 CFR 1.98(b)) October 14, 2003 2823 **U.S. PATENT DOCUMENTS EXAMINER FILING** INITIALS PATENT NO. DATE **PATENTEE CLASS CLASS** DATE Α 6,211,559 B1 04/03/03 Zhu et al. K.N. 257 02/27/98 В C D Ε F G **FOREIGN PATENT DOCUMENT** SUB TRANSL. DOCUMENT NO. DATE COUNTRY **CLASS** CLASS YESINO J 1 061 592 A2 12/20/00 Europe K.N. X K 99/14760 03/25/99 **WIPO** K.N. Х L M OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) Ō Roy E. Scheuerlein: "Magneto-Resistive IC Memory Limitations and K.N. Architecture Implications", Proceedings of the 7th IEEE International Nonvolatile Memory Technology Conference", June 22-24, 1998, pp. 47-50 P S. Tehrani et al.: "Recent Developments in Magnetic Tunnel Junction MRAM". K.M. IEEE Transactions on Magnetics, Vol. 36, No. 5, September 2000, pp. 2752-2757 **EXAMINER** DATE CONSIDERED EXAMINER: Initial if oration considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.